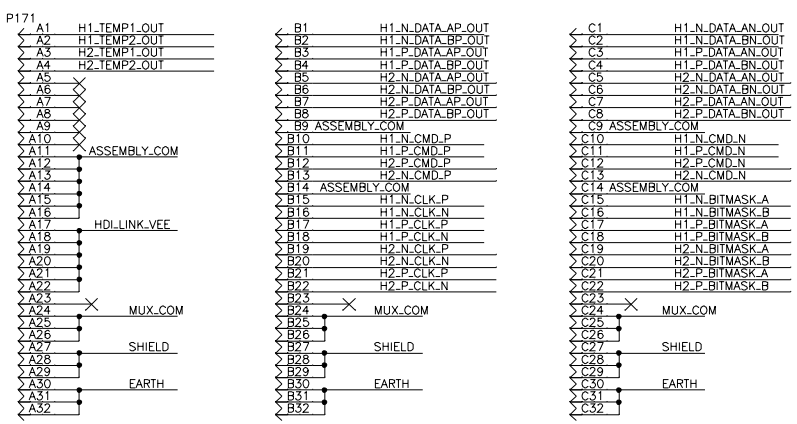
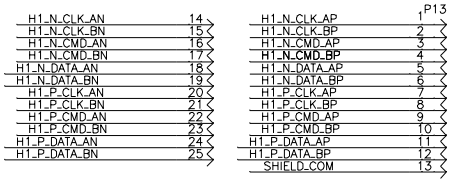
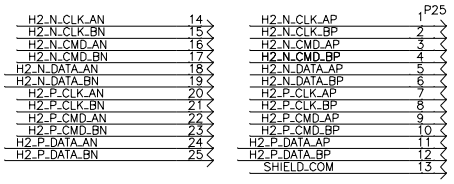


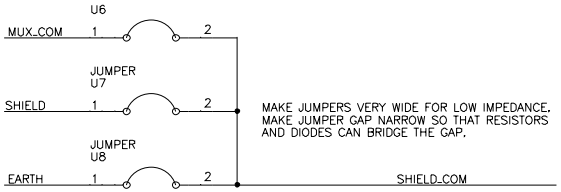
NOTE: THIS CONNECTOR IS POSITIONED
UPSIDE DOWN AND ON THE OPPOSITE SIDE
OF THE PC BOARD FROM THE NORMAL VME CONVENTION.



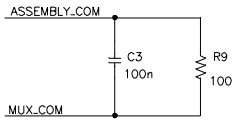
25-pin Signal Cable Connector
to Matching Card signal connector
HDI Module #1

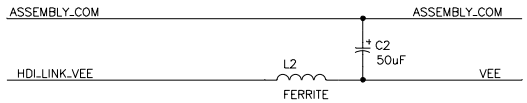


25-pin Signal Cable Connector
to Matching Card signal connector
HDI Module #2



MAKE JUMPERS VERY WIDE FOR LOW IMPEDANCE.
MAKE JUMPER GAP NARROW SO THAT RESISTORS
AND DIODES CAN BRIDGE THE GAP.





DIGITAL SIGNAL NAME FORMAT:

NAME = HU_V_W_{X}{Y}{_Z}

U = 1,2 (HDI DETECTOR MODULE NUMBER).
V = N (N-SIDE), P (P-SIDE).
W = CLK, CMD, DATA, BITMASK.
X = A (CHANNEL A), B (CHANNEL B), NOT SPECIFIED.
Y = N (INVERTING SIGNAL), P (NON-INVERTING SIGNAL), NOT SPECIFIED.
Z = IN, OUT, NOT SPECIFIED.

NOTE: SIGNAL NAMES ENDING IN "_COM" ARE COMMON TO ONE OR MORE
MODULES, AND DO NOT FOLLOW THE ABOVE NAMING CONVENTION.

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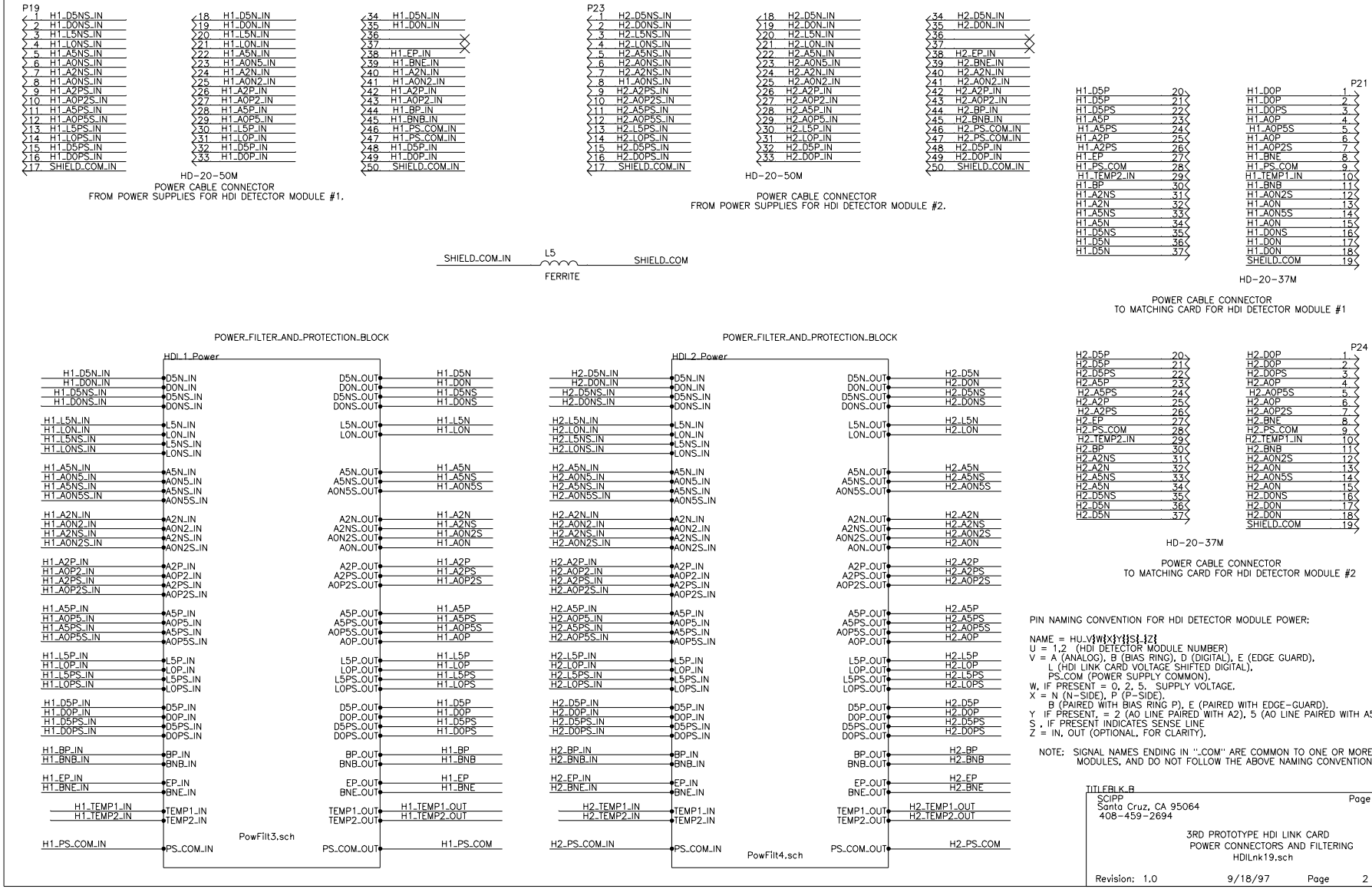
P19
H1_D5NS_IN
H1_D0NS_IN
H1_L5NS_IN
H1_L0NS_IN
H1_A5NS_IN
H1_A0NS_IN
H1_A2NS_IN
H1_A0NS_IN
H1_A2PS_IN
H1_A0P2S_IN
H1_A5PS_IN
H1_A0P5S_IN
H1_L5PS_IN
H1_L0PS_IN
H1_D5PS_IN
H1_D0PS_IN
SHIELD_COM_IN
H1_D5N_IN
H1_D0N_IN
H1_L5N_IN
H1_L0N_IN
H1_A5N_IN
H1_A0N5_IN
H1_A2N_IN
H1_A0N2_IN
H1_A2P_IN
H1_A0P2_IN
H1_A5P_IN
H1_A0P5_IN
H1_L5P_IN
H1_L0P_IN
H1_D5P_IN
H1_D0P_IN
H1_BP_IN
H1_BNB_IN
H1_EP_IN
H1_BNE_IN
H1_PS_COM_IN
H1_D5N_IN
H1_D0N_IN
H1_D5NS_IN
H1_D0NS_IN
H1_EP_IN
H1_BNE_IN
H1_A2N_IN
H1_A0N2_IN
H1_A2P_IN
H1_A0P2_IN
H1_BP_IN
H1_BNB_IN
H1_PS_COM_IN
H1_D5P_IN
H1_D0P_IN
SHIELD_COM_IN
HD-20-50M
POWER CABLE CONNECTOR
FROM POWER SUPPLIES FOR HDI DETECTOR MODULE #1.
P23
H2_D5NS_IN
H2_D0NS_IN
H2_L5NS_IN
H2_L0NS_IN
H2_A5NS_IN
H2_A0NS_IN
H2_A2NS_IN
H2_A0NS_IN
H2_A2PS_IN
H2_A0P2S_IN
H2_A5PS_IN
H2_A0P5S_IN
H2_L5PS_IN
H2_L0PS_IN
H2_D5PS_IN
H2_D0PS_IN
SHIELD_COM_IN
H2_D5N_IN
H2_D0N_IN
H2_L5N_IN
H2_L0N_IN
H2_A5N_IN
H2_A0N5_IN
H2_A2N_IN
H2_A0N2_IN
H2_A2P_IN
H2_A0P2_IN
H2_A5P_IN
H2_A0P5_IN
H2_L5P_IN
H2_L0P_IN
H2_D5P_IN
H2_D0P_IN
H2_BP_IN
H2_BNB_IN
H2_EP_IN
H2_BNE_IN
H2_PS_COM_IN
H2_D5N_IN
H2_D0N_IN
H2_D5NS_IN
H2_D0NS_IN
H2_EP_IN
H2_BNE_IN
H2_A2N_IN
H2_A0N2_IN
H2_A2P_IN
H2_A0P2_IN
H2_BP_IN
H2_BNB_IN
H2_PS_COM_IN
H2_D5P_IN
H2_D0P_IN
SHIELD_COM_IN
HD-20-50M
POWER CABLE CONNECTOR
FROM POWER SUPPLIES FOR HDI DETECTOR MODULE #2.
P21
H1_D5P
H1_D5P
H1_D5PS
H1_A5P
H1_A5PS
H1_A2P
H1_A2PS
H1_EP
H1_PS_COM
H1_TEMP2_IN
H1_BP
H1_A2NS
H1_A2N
H1_A5NS
H1_A5N
H1_D5NS
H1_D5N
H1_D0P
H1_D0P
H1_D0PS
H1_A0P
H1_A0P5S
H1_A0P
H1_A0P2S
H1_BNE
H1_PS_COM
H1_TEMP1_IN
H1_BNB
H1_A0N2S
H1_A0N
H1_A0N5S
H1_A0N
H1_D0NS
H1_D0N
H1_D0N
SHIELD_COM
HD-20-37M
POWER CABLE CONNECTOR
TO MATCHING CARD FOR HDI DETECTOR MODULE #1
SHIELD_COM_IN
L5
SHIELD_COM
FERRITE
POWER_FILTER_AND_PROTECTION_BLOCK
HDI_1_Power
H1_D5N_IN
H1_D0N_IN
H1_D5NS_IN
H1_D0NS_IN
H1_L5N_IN
H1_L0N_IN
H1_L5NS_IN
H1_L0NS_IN
H1_A5N_IN
H1_A0N5_IN
H1_A5NS_IN
H1_A0N5S_IN
H1_A2N_IN
H1_A0N2_IN
H1_A2NS_IN
H1_A0N2S_IN
H1_A2P_IN
H1_A0P2_IN
H1_A2PS_IN
H1_A0P2S_IN
H1_A5P_IN
H1_A0P5_IN
H1_A5PS_IN
H1_A0P5S_IN
H1_L5P_IN
H1_L0P_IN
H1_L5PS_IN
H1_L0PS_IN
H1_D5P_IN
H1_D0P_IN
H1_D5PS_IN
H1_D0PS_IN
H1_BP_IN
H1_BNB_IN
H1_EP_IN
H1_BNE_IN
H1_TEMP1_IN
H1_TEMP2_IN
H1_PS_COM_IN
D5N_IN
D0N_IN
D5NS_IN
D0NS_IN
L5N_IN
L0N_IN
L5NS_IN
L0NS_IN
A5N_IN
A0N5_IN
A5NS_IN
A0N5S_IN
A2N_IN
A0N2_IN
A2NS_IN
A0N2S_IN
A2P_IN
A0P2_IN
A2PS_IN
A0P2S_IN
A5P_IN
A0P5_IN
A5PS_IN
A0P5S_IN
L5P_IN
L0P_IN
L5PS_IN
L0PS_IN
D5P_IN
D0P_IN
D5PS_IN
D0PS_IN
BP_IN
BNB_IN
EP_IN
BNE_IN
TEMP1_IN
TEMP2_IN
PS_COM_IN
D5N_OUT
D0N_OUT
D5NS_OUT
D0NS_OUT
L5N_OUT
L0N_OUT
A5N_OUT
A5NS_OUT
A0N5S_OUT
A2N_OUT
A2NS_OUT
A0N2S_OUT
A0N_OUT
A2P_OUT
A2PS_OUT
A0P2S_OUT
A5P_OUT
A5PS_OUT
A0P5S_OUT
A0P_OUT
L5P_OUT
L0P_OUT
L5PS_OUT
L0PS_OUT
D5P_OUT
D0P_OUT
D5PS_OUT
D0PS_OUT
BP_OUT
BNB_OUT
EP_OUT
BNE_OUT
TEMP1_OUT
TEMP2_OUT
PS_COM_OUT
H1_D5N
H1_D0N
H1_D5NS
H1_D0NS
H1_L5N
H1_L0N
H1_A5N
H1_A5NS
H1_A0N5S
H1_A2N
H1_A2NS
H1_A0N2S
H1_A0N
H1_A2P
H1_A2PS
H1_A0P2S
H1_A5P
H1_A0P5S
H1_A0P
H1_L5P
H1_L0P
H1_L5PS
H1_L0PS
H1_D5P
H1_D0P
H1_D5PS
H1_D0PS
H1_BP
H1_BNB
H1_EP
H1_BNE
H1_TEMP1_OUT
H1_TEMP2_OUT
H1_PS_COM
PowFilt3.sch
POWER_FILTER_AND_PROTECTION_BLOCK
HDI_2_Power
H2_D5N_IN
H2_D0N_IN
H2_D5NS_IN
H2_D0NS_IN
H2_L5N_IN
H2_L0N_IN
H2_L5NS_IN
H2_L0NS_IN
H2_A5N_IN
H2_A0N5_IN
H2_A5NS_IN
H2_A0N5S_IN
H2_A2N_IN
H2_A0N2_IN
H2_A2NS_IN
H2_A0N2S_IN
H2_A2P_IN
H2_A0P2_IN
H2_A2PS_IN
H2_A0P2S_IN
H2_A5P_IN
H2_A0P5_IN
H2_A5PS_IN
H2_A0P5S_IN
H2_L5P_IN
H2_L0P_IN
H2_L5PS_IN
H2_L0PS_IN
H2_D5P_IN
H2_D0P_IN
H2_D5PS_IN
H2_D0PS_IN
H2_BP_IN
H2_BNB_IN
H2_EP_IN
H2_BNE_IN
H2_TEMP1_IN
H2_TEMP2_IN
H2_PS_COM_IN
D5N_IN
D0N_IN
D5NS_IN
D0NS_IN
L5N_IN
L0N_IN
L5NS_IN
L0NS_IN
A5N_IN
A0N5_IN
A5NS_IN
A0N5S_IN
A2N_IN
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A0P5_IN
A5PS_IN
A0P5S_IN
L5P_IN
L0P_IN
L5PS_IN
L0PS_IN
D5P_IN
D0P_IN
D5PS_IN
D0PS_IN
BP_IN
BNB_IN
EP_IN
BNE_IN
TEMP1_IN
TEMP2_IN
PS_COM_IN
D5N_OUT
D0N_OUT
D5NS_OUT
D0NS_OUT
L5N_OUT
L0N_OUT
A5N_OUT
A5NS_OUT
A0N5S_OUT
A2N_OUT
A2NS_OUT
A0N2S_OUT
A0N_OUT
A2P_OUT
A2PS_OUT
A0P2S_OUT
A5P_OUT
A5PS_OUT
A0P5S_OUT
A0P_OUT
L5P_OUT
L0P_OUT
L5PS_OUT
L0PS_OUT
D5P_OUT
D0P_OUT
D5PS_OUT
D0PS_OUT
BP_OUT
BNB_OUT
EP_OUT
BNE_OUT
TEMP1_OUT
TEMP2_OUT
PS_COM_OUT
H2_D5N
H2_D0N
H2_D5NS
H2_D0NS
H2_L5N
H2_L0N
H2_A5N
H2_A5NS
H2_A0N5S
H2_A2N
H2_A2NS
H2_A0N2S
H2_A0N
H2_A2P
H2_A2PS
H2_A0P2S
H2_A5P
H2_A0P5S
H2_A0P
H2_L5P
H2_L0P
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H2_D5PS
H2_D0PS
H2_BP
H2_BNB
H2_EP
H2_BNE
H2_TEMP1_OUT
H2_TEMP2_OUT
H2_PS_COM
PowFilt4.sch
P24
H2_D5P
H2_D5P
H2_D5PS
H2_A5P
H2_A5PS
H2_A2P
H2_A2PS
H2_EP
H2_PS_COM
H2_TEMP2_IN
H2_BP
H2_A2NS
H2_A2N
H2_A5NS
H2_A5N
H2_D5NS
H2_D5N
H2_D0P
H2_D0P
H2_D0PS
H2_A0P
H2_A0P5S
H2_A0P
H2_A0P2S
H2_BNE
H2_PS_COM
H2_TEMP1_IN
H2_BNB
H2_A0N2S
H2_A0N
H2_A0N5S
H2_A0N
H2_D0NS
H2_D0N
H2_D0N
SHIELD_COM
HD-20-37M
POWER CABLE CONNECTOR
TO MATCHING CARD FOR HDI DETECTOR MODULE #2
PIN NAMING CONVENTION FOR HDI DETECTOR MODULE POWER:
NAME = HU_V{W{X}Y{}S{_}Z{
U = 1,2 (HDI DETECTOR MODULE NUMBER)
V = A (ANALOG), B (BIAS RING), D (DIGITAL), E (EDGE GUARD),
L (HDI LINK CARD VOLTAGE SHIFTED DIGITAL),
PS_COM (POWER SUPPLY COMMON).
W, IF PRESENT = 0, 2, 5. SUPPLY VOLTAGE.
X = N (N-SIDE), P (P-SIDE).
B (PAIRED WITH BIAS RING P), E (PAIRED WITH EDGE-GUARD).
Y IF PRESENT, = 2 (AO LINE PAIRED WITH A2), 5 (AO LINE PAIRED WITH A5),
S , IF PRESENT INDICATES SENSE LINE
Z = IN, OUT (OPTIONAL, FOR CLARITY).
NOTE: SIGNAL NAMES ENDING IN "_COM" ARE COMMON TO ONE OR MORE MODULES, AND DO NOT FOLLOW THE ABOVE NAMING CONVENTION.
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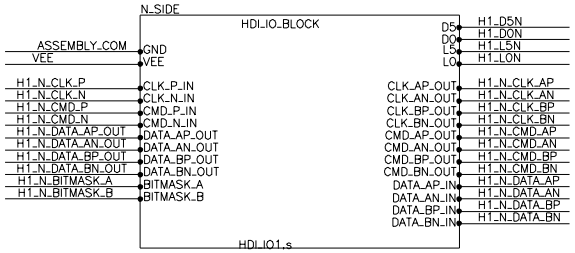
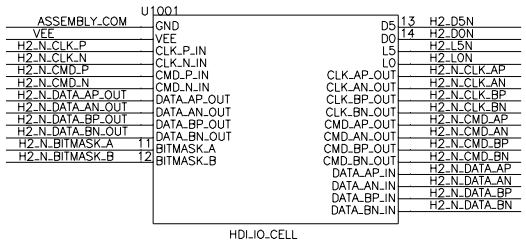
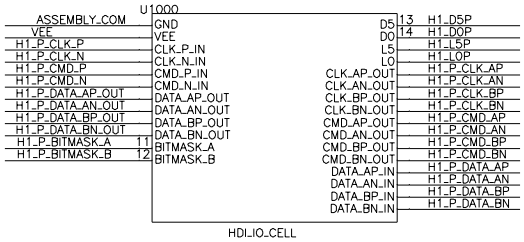
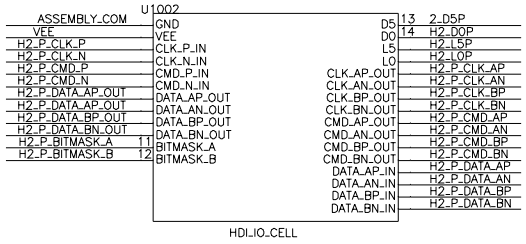


HDI MODULE #1 N-SIDE CIRCUITRY.
THIS BLOCK IS IDENTICAL TO #1 P-SIDE CIRCUITRY,
#2 N-SIDE, AND #2 P-SIDE CIRCUITRY.
THESE MODULES WILL BE "GERBER GHOSTED".







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